

ISL9K8120P3

8A, 1200V Stealth™ Dual Diode

General Description

The ISL9K8120P3 is a Stealth™ dual diode optimized for low loss performance in high frequency hard switched applications. The Stealth™ family exhibits low reverse recovery current ($I_{RM(REC)}$) and exceptionally soft recovery under typical operating conditions.

This device is intended for use as a free wheeling or boost diode in power supplies and other power switching applications. The low $I_{RM(REC)}$ and short t_a phase reduce loss in switching transistors. The soft recovery minimizes ringing, expanding the range of conditions under which the diode may be operated without the use of additional snubber circuitry. Consider using the Stealth™ diode with a 1200V NPT IGBT to provide the most efficient and highest power density design at lower cost.

Formerly developmental type TA49413.

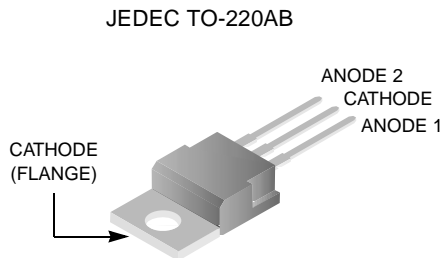
Features

- Soft Recovery $t_b / t_a > 5.5$
- Fast Recovery $t_{rr} < 32ns$
- Operating Temperature 150°C
- Reverse Voltage 1200V
- Avalanche Energy Rated

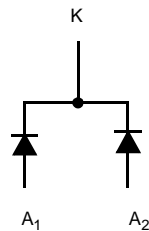
Applications

- Switch Mode Power Supplies
- Hard Switched PFC Boost Diode
- UPS Free Wheeling Diode
- Motor Drive FWD
- SMPS FWD
- Snubber Diode

Package



Symbol



Device Maximum Ratings (per leg) $T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Ratings	Units
V_{RRM}	Repetitive Peak Reverse Voltage	1200	V
V_{RWM}	Working Peak Reverse Voltage	1200	V
V_R	DC Blocking Voltage	1200	V
$I_{F(AV)}$	Average Rectified Forward Current ($T_C = 105^\circ\text{C}$) Total Device Current (Both Legs)	8 16	A A
I_{FRM}	Repetitive Peak Surge Current (20kHz Square Wave)	16	A
I_{FSM}	Nonrepetitive Peak Surge Current (Halfwave 1 Phase 60Hz)	100	A
P_D	Power Dissipation	71	W
E_{AVL}	Avalanche Energy (1A, 40mH)	20	mJ
T_J, T_{STG}	Operating and Storage Temperature Range	-55 to 150	°C
T_L T_{PKG}	Maximum Temperature for Soldering Leads at 0.063in (1.6mm) from Case for 10s Package Body for 10s, See Application Note AN-7528	300 260	°C °C

CAUTION: Stresses above those listed in "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.

Package Marking and Ordering Information

Device Marking	Device	Package	Tape Width	Quantity
K8120P3	ISL9K8120P3	TO-220AB	N/A	50

Electrical Characteristics (per leg) $T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
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Off State Characteristics

I_R	Instantaneous Reverse Current	$V_R = 1200\text{V}$	$T_C = 25^\circ\text{C}$	-	-	100	μA
			$T_C = 125^\circ\text{C}$	-	-	1.0	mA

On State Characteristics

V_F	Instantaneous Forward Voltage	$I_F = 8\text{A}$	$T_C = 25^\circ\text{C}$	-	2.8	3.3	V
			$T_C = 125^\circ\text{C}$	-	2.7	3.1	V

Dynamic Characteristics

C_J	Junction Capacitance	$V_R = 10\text{V}, I_F = 0\text{A}$	-	30	-	pF
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Switching Characteristics

t_{rr}	Reverse Recovery Time	$I_F = 1\text{A}, di_F/dt = 100\text{A}/\mu\text{s}, V_R = 30\text{V}$	-	25	32	ns
		$I_F = 8\text{A}, di_F/dt = 100\text{A}/\mu\text{s}, V_R = 30\text{V}$	-	35	44	ns
t_{rr}	Reverse Recovery Time	$I_F = 8\text{A},$	-	300	-	ns
$I_{RM(REC)}$	Maximum Reverse Recovery Current	$di_F/dt = 200\text{A}/\mu\text{s},$	-	4.3	-	A
Q_{RR}	Reverse Recovered Charge	$V_R = 780\text{V}, T_C = 25^\circ\text{C}$	-	525	-	nC
t_{rr}	Reverse Recovery Time	$I_F = 8\text{A},$	-	375	-	ns
S	Softness Factor (t_b/t_a)	$di_F/dt = 200\text{A}/\mu\text{s},$	-	9	-	-
$I_{RM(REC)}$	Maximum Reverse Recovery Current	$V_R = 780\text{V},$	-	5.5	-	A
Q_{RR}	Reverse Recovered Charge	$T_C = 125^\circ\text{C}$	-	1.1	-	μC
t_{rr}	Reverse Recovery Time	$I_F = 8\text{A},$	-	200	-	ns
S	Softness Factor (t_b/t_a)	$di_F/dt = 1000\text{A}/\mu\text{s},$	-	5.5	-	-
$I_{RM(REC)}$	Maximum Reverse Recovery Current	$V_R = 780\text{V},$	-	11	-	A
Q_{RR}	Reverse Recovered Charge	$T_C = 125^\circ\text{C}$	-	1.2	-	μC
di_M/dt	Maximum di/dt during t_b		-	310	-	$\text{A}/\mu\text{s}$

Thermal Characteristics

$R_{\theta JC}$	Thermal Resistance Junction to Case	TO-220	-	-	1.75	$^\circ\text{C}/\text{W}$
$R_{\theta JA}$	Thermal Resistance Junction to Ambient	TO-220	-	-	62	$^\circ\text{C}/\text{W}$

Typical Performance Curves (per leg)

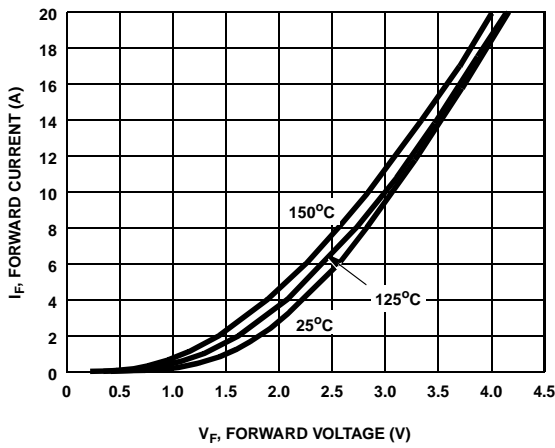


Figure 1. Forward Current vs Forward Voltage

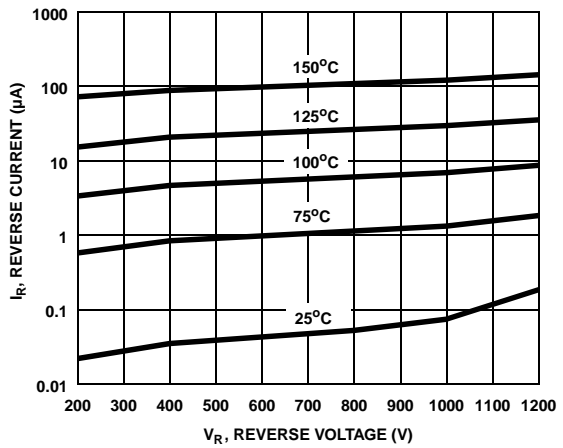


Figure 2. Reverse Current vs Reverse Voltage

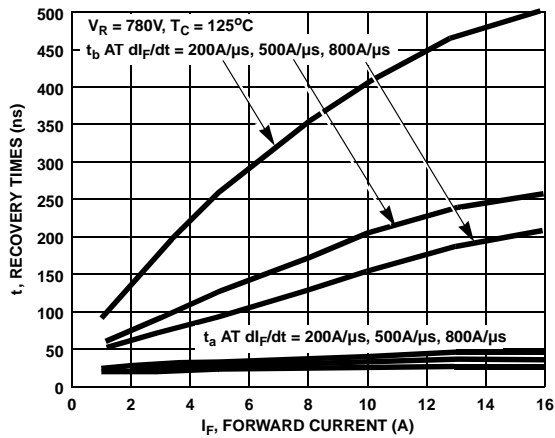


Figure 3. t_a and t_b Curves vs Forward Current

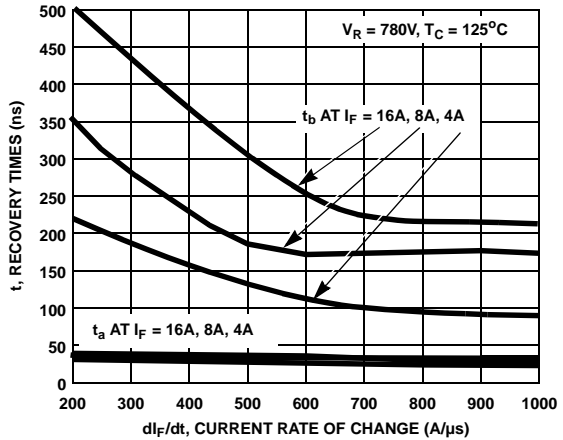


Figure 4. t_a and t_b Curves vs di_F/dt

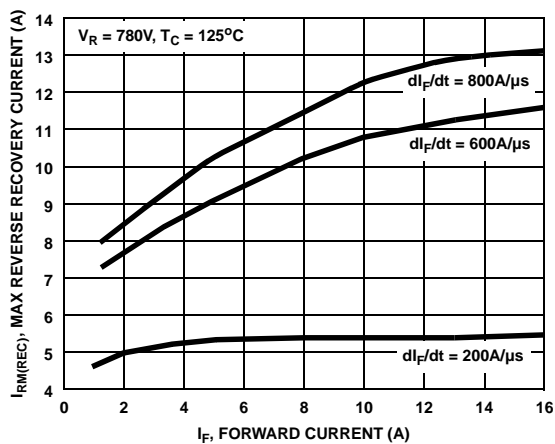


Figure 5. Maximum Reverse Recovery Current vs Forward Current

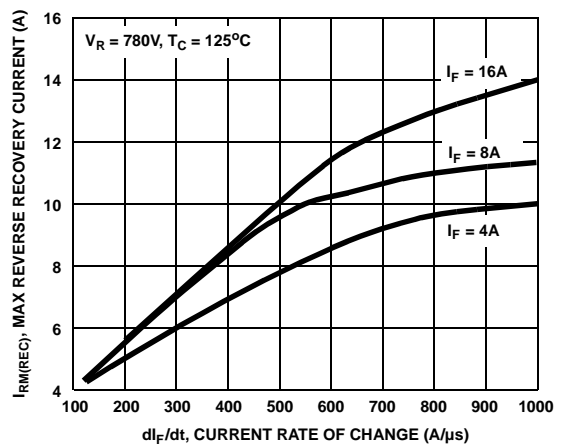


Figure 6. Maximum Reverse Recovery Current vs di_F/dt

Typical Performance Curves (per leg) (Continued)

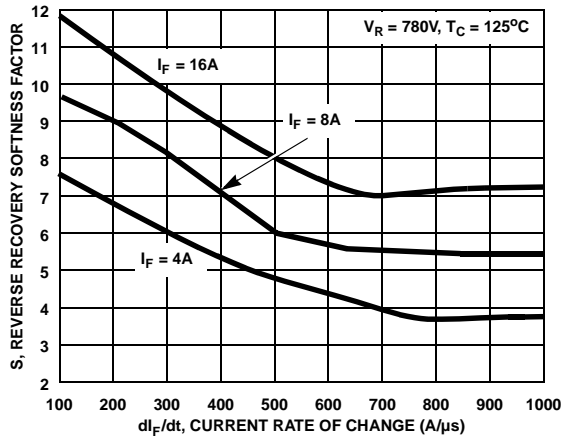


Figure 7. Reverse Recovery Softness Factor vs diF/dt

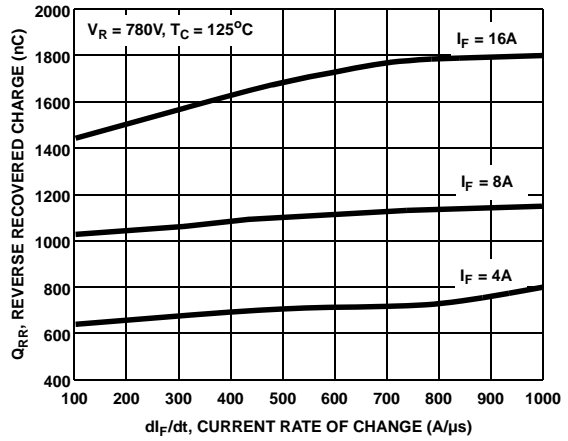


Figure 8. Reverse Recovered Charge vs diF/dt

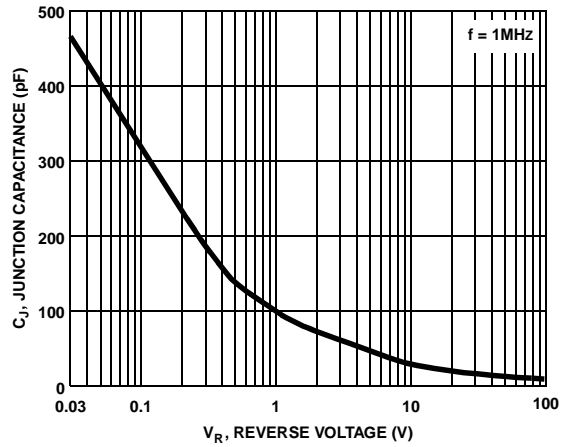


Figure 9. Junction Capacitance vs Reverse Voltage

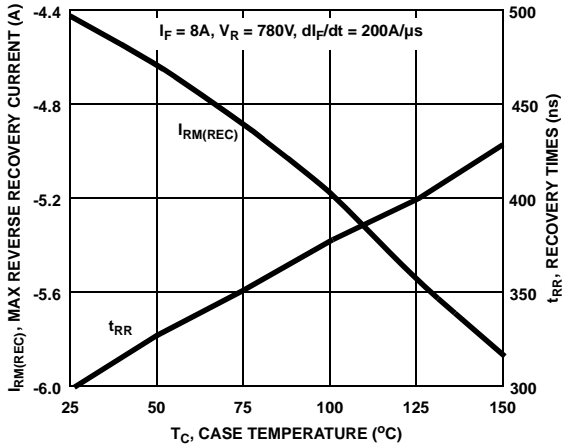


Figure 10. Reverse Recovery Current and Times vs Case Temperature

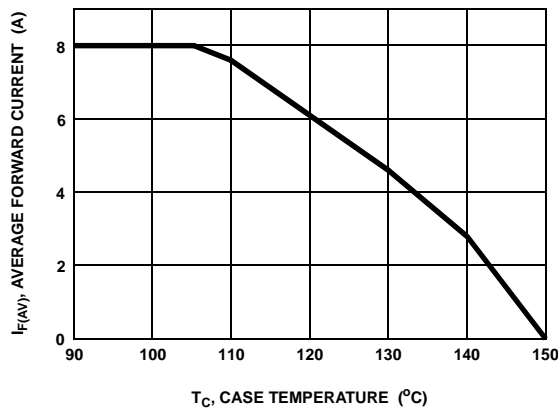


Figure 11. DC Current Derating Curve

Typical Performance Curves (per leg) (Continued)

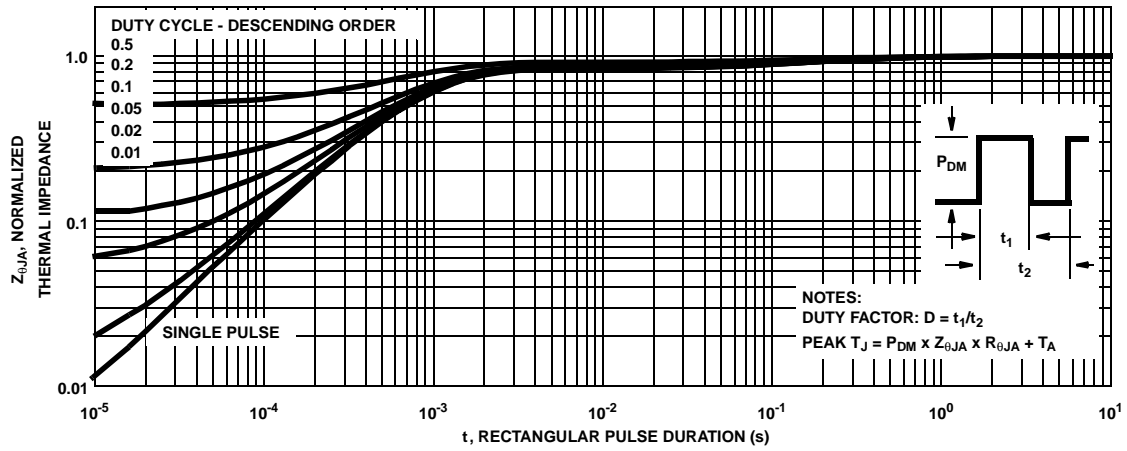


Figure 12. Normalized Maximum Transient Thermal Impedance

Test Circuit and Waveforms

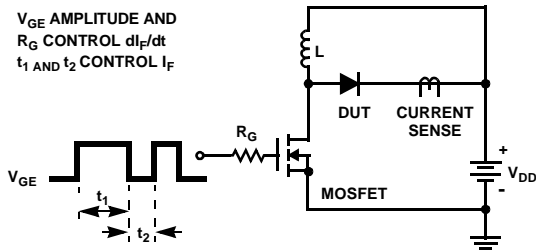


Figure 13. t_{rr} Test Circuit

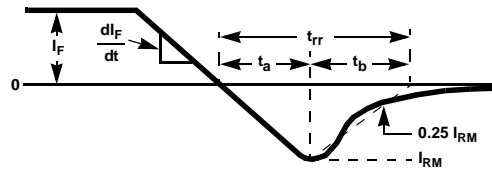


Figure 14. t_{rr} Waveforms and Definitions

$I = 1A$
 $L = 40mH$
 $R < 0.1\Omega$
 $V_{DD} = 50V$
 $E_{AVL} = 1/2LI^2 [V_{R(AVL)}/(V_{R(AVL)} - V_{DD})]$
 $Q_1 = IGBT (BV_{CES} > DUT V_{R(AVL)})$

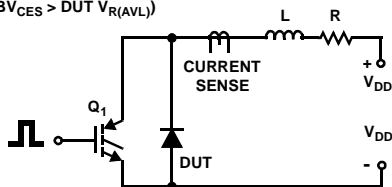


Figure 15. Avalanche Energy Test Circuit

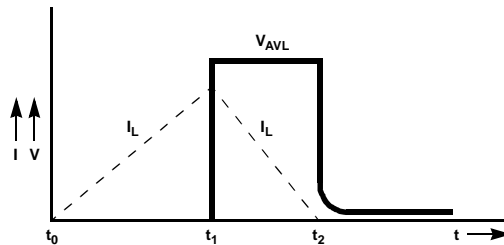


Figure 16. Avalanche Current and Voltage Waveforms

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